

# ● PRINTER RUSH ●

(PTO ASSISTANCE)

Application : 09/434082 Examiner : Peikari GAU : 2186

From: PAP Location: IDC FMF FDC Date: 11/8/04

Tracking #: 06014540 Week Date: 9/27/04

DOC CODE	DOC DATE	MISCELLANEOUS
<input checked="" type="checkbox"/> 1449	<u>12/23/02</u>	<input type="checkbox"/> Continuing Data
<input type="checkbox"/> IDS		<input type="checkbox"/> Foreign Priority
<input type="checkbox"/> CLM		<input type="checkbox"/> Document Legibility
<input type="checkbox"/> IIFW		<input type="checkbox"/> Fees
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<input type="checkbox"/> DRW		
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Sheet 1 of 1

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Complete if Known	
<b>Application Number</b>	09/434082
<b>Filing Date</b>	November 5, 1999
<b>First Named Inventor</b>	Ryan, Kevin
<b>Group Art Unit</b>	2186
<b>Examiner Name</b>	Peikari, James

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Attorney Docket No: 303.306US2

Technology Center 2100

**US PATENT DOCUMENTS**

Examiner Initials*	USP Document Number	Publication Date	Name of Patentee or Applicant of cited Document	Class	Subclass	Filing Date If Appropriate
	US-5,270,964	12/14/1993	Bechtolsheim, A., et al	365	52	05/19/1992
	US-5,598,545	01/28/1997	Childers, J., et al	395	562	03/25/1993
	US-5,625,780	04/29/1997	Hsieh, W., et al	395	311	11/02/1994
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	US-5,903,509	05/11/1999	Ryan, K., et al	365	230.03	05/04/1998
	US-6,172,893	01/09/2001	Ryan, K.	365	49	01/05/1999

**FOREIGN PATENT DOCUMENTS**

Examiner Initials*	Foreign Document No	Publication Date	Name of Patentee or Applicant of cited Document	Class	Subclass	T <sup>2</sup>

**OTHER DOCUMENTS -- NON PATENT LITERATURE DOCUMENTS**

Examiner Initials*	Cite No. <sup>1</sup>	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.	T <sup>2</sup>
		"400 Mb/s/pin SDRAM, 4M x 18 SDRAM, Pipelined, Eight Bank, 2.5V Operation", SDRAM Inc., Draft/Advance, SLD4M18DR400, 4 MEG x 18 SDRAM,(1998),pp. 1-69	
		"Draft Standard for A High-Speed Memory Interface (SyncLink)", The Institute of Electrical and Electronics Engineers, Inc., Draft 0.99 IEEE P1596.7-199X,(1996),pp. 1-55	
		"New NEC Memory Technology Increases Performance by 20%", http://www.nbpacifica.com/headline/newnecmemorytechnolo_1144.sh,(1997),pp. 1-2	
		"Virtual Channel Memory", NEC Electronics Inc., (1997),pp. 1-2	
		GILLINGHAM, P., "SLDRAM Architectural and Functional Overview", The SDRAM Consortium, (Aug. 1997),pp. 1-14	

**EXAMINER****DATE CONSIDERED**

Substitute Disclosure Statement Form (PTO-1449)

\* EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609. Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant. <sup>1</sup> Applicant's unique citation designation number (optional) <sup>2</sup> Applicant is to place a check mark here if English language Translation is attached